



Ulrich Hensel is a design methodology architect in the GFs design enablement organization. He is overseeing the scope, content and architecture of design guidelines and reference flows for GFs design platform across all of GF's technology nodes optimizing the customers product metrics, time to market, and volume yield.

His special focus is on solutions for low power, energy efficiency, and automotive.

Since 2018, he is working on an effective and consistent automotive design enablement platform for 22FDX, 40LP-ESF3H, 12LP-PLUS and other nodes.

Before GLOBALFOUNDRIES was formed out of AMD, Ulrich worked in several digital design and design verification roles, contributing to wireless communication, southbridge, northbridge and PCIE designs.

He received his Diploma and Doctorate degree in Computer Science from the University of Technology in Dresden.